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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)			Application Number	10/814,818-Conf. #5283	
			Filing Date	March 31, 2004	
			First Named Inventor	Pierre Busson	
			Art Unit	2136	
			Examiner Name	O. A. Louie	
Sheet	1	of	1	Attorney Docket Number	361170-1029

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	† <sup>3</sup>
		Country Code <sup>4</sup> -Number <sup>5</sup> -Kind Code <sup>6</sup> (if known)				

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NON PATENT LITERATURE DOCUMENTS						
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				† <sup>3</sup>
	C1	L. Tan, et al., ISSCC 98/Paper FP 13.1: A 70Mb/s variable-rate 1024-QAM Cable receiver IC with Integrated 10b ADC and FEC decoder, 02/06/1998.				
	C2	L. Tan, et al., "A 70-Mb/s Variable-Rate 1024-QAM Cable Receiver IC with Integrated 10-b ADC and FEC Decoder, IEEE Journal of Solid-State Circuits, Vol. 33, No. 12, pps. 2205-2218, dated December 1998.				

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